

Hardware Model Checking Competition

CAV 2014 Edition

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The results of the 7th International Hardware Model Checking Competition were presented at CAV 2014. Model checkers were required to produce witnesses for single safety properties. The traces were checked by the AIGSIM tool, which is part of the AIGER tools. Otherwise, the competition was run in almost the same way as in the previous two years. The competition was run on a cluster at Aalto University with exclusive access to 32 nodes of 2x Six-Core AMD Opteron 2435 2.6GHz with at least 16 GB of RAM. This meant 12 cores for each solver per benchmark, memory limit of 15 GB and time limit of 900 seconds. As fall back we had the cluster at JKU with the same characteristics as in previous years. Beside the requirement to produce witnesses, rules, input and output format did not change.

During the FLoC Olympic Games ceremony in the second week, three real silver medals were handed out to the winners of the three tracks of the competition. These three tracks in the CAV 2014 edition of the competition consisted of: the single safety, the liveness and the deep bound track. There was no multiple property track for the CAV edition. It was considered to be the technically most challenging track, particularly while moving to new hardware, and further, only three medals were available. The winner of the deep bound track received both a medal and check, sponsored again by Oski technology. The competition further relied on support by the national research network on Rigorous System Engineering (RiSE) funded by the Austrian Science Fund (FWF) and also used resources made available through the Science-IT project at Aalto University.